MICDE and the Department of Electrical Engineering and Computer Science present:

Scalability, Performance, Stability, and Portability of Many-core Computing Algorithms

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2 pm | March 19 | 1504 GG Brown (2350 Hayward)

Abstract: The IMPACT group at the University of Illinois has been working on the co-design of scalable algorithms and programming tools for massively threaded computing.

A major challenge that we are addressing is to simultaneously achieve scalability, performance, numerical stability, portability, and development cost.

In this talk, Dr. Hwu will go over the major building blocks involved: memory layout and dynamic tiling. He will show experimental results to demonstrate how these building blocks jointly enable the first scalable, numerically stable tri-diagonal solver that matches the numerical stability of the Intel Math Kernel Library (MKL) and surpass the performance of CUSPARSE.

He will then give an overview of the Tangram and Triolet projects that are aimed to drastically improve the quality and reduce the development and maintenance cost future many-core algorithms.

Dr. Hwu’s research interests are in the area of architecture, implementation, compilation, and algorithms for parallel computing. He is the chief scientist of Parallel Computing Institute and director of the IMPACT research group, and a co-founder and CTO of MulticoreWare. For his contributions in research and teaching, he received the ACM SigArch Maurice Wilkes Award, the ACM Grace Murray Hopper Award, the Tau Beta Pi Daniel C. Drucker Eminent Faculty Award, the ISCA Influential Paper Award, and the Distinguished Alumni Award in Computer Science of the University of California, Berkeley. He is a fellow of IEEE and ACM. He directs the UIUC CUDA Center of Excellence and serves as one of the PIs of the $208M NSF Blue Waters Petascale project.

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